

Day : Tuesday
Date: 11/22/2005

PALM INTRANET

Time: 12:21:51

Inventor Name Search Result

Your Search was:

Last Name = LAVIGNE

First Name = BRUCE

Application#	Patent#	Status	Date Filed	Title	Inventor Name
<u>10091837</u>	Not Issued	30	03/05/2002	System for performing input processing on a data packet	LAVIGNE, BRUCE
<u>11198056</u>	Not Issued	30	08/05/2005	Prioritization of network traffic sent to a processor by using packet importance	LAVIGNE, BRUCE
<u>10091691</u>	Not Issued	30	03/05/2002	System and method for forwarding packets	LAVIGNE, BRUCE E.
<u>10091694</u>	Not Issued	30	03/05/2002	Network device for sampling a packet	LAVIGNE, BRUCE E.
<u>10091768</u>	6954812	150	03/05/2002	TWO-STAGE ROUND ROBIN ARBITRATION SYSTEM	LAVIGNE, BRUCE E.
<u>10091874</u>	Not Issued	94	03/05/2002	FORWARDING ORDERED DATA PACKETS WITH LESS DELAY	LAVIGNE, BRUCE E.
<u>10091875</u>	Not Issued	30	03/05/2002	System and method for resolving wraparound ambiguity in a counter	LAVIGNE, BRUCE E.
<u>10092155</u>	Not Issued	30	03/05/2002	Method and apparatus for computing, storing, and multiplexing modified bytes in a packet router	LAVIGNE, BRUCE E.
<u>11051422</u>	Not Issued	30	02/04/2005	Conduit port for network chips	LAVIGNE, BRUCE E.
<u>11054646</u>	Not Issued	20	02/08/2005	Support chip for handling network chips on a network device	LAVIGNE, BRUCE E.
<u>11088394</u>	Not Issued	30	03/23/2005	Encapsulating packets for network chip conduit port	LAVIGNE, BRUCE E.
<u>11102973</u>	Not Issued	30	04/11/2005	Packet protection for header modification	LAVIGNE, BRUCE E.
<u>11203362</u>	Not Issued	30	08/12/2005	Screening of calls in a packet-based network	LAVIGNE, BRUCE E.

<u>09096143</u>	<u>6289066</u>	150	06/11/1998	METHOD AND APPARATUS FOR RECENTERING AN ELASTICITY FIFO WHEN RECEIVING 1000BASE-X TRAFFIC USING MINIMAL INFORMATION	LAVIGNE, BRUCE E.
<u>10091775</u>	Not Issued	30	03/05/2002	System and method for speculatively issuing memory requests while maintaining a specified packet order	LAVIGNE, BRUCE E.
<u>11212146</u>	Not Issued	20	08/26/2005	Round robin arbitration system	LAVIGNE, BRUCE E.
<u>10723041</u>	Not Issued	20	11/26/2003	Remote mirroring using IP encapsulation	LAVIGNE, BRUCE EDWARD
<u>10813730</u>	Not Issued	20	03/31/2004	Secure remote mirroring	LAVIGNE, BRUCE EDWARD
<u>10813766</u>	Not Issued	30	03/31/2004	Methods and apparatus for selection of mirrored traffic	LAVIGNE, BRUCE EDWARD

Inventor Search Completed: No Records to Display.

Search Another: Inventor	Last Name	First Name	Search
	LAVIGNE	BRUCE	

To go back use Back button on your browser toolbar.

Back to [PALM](#) | [ASSIGNMENT](#) | [OASIS](#) | [Home page](#)

Day : Tuesday
Date: 11/22/2005


PALM INTRANET

Time: 12:22:39

Inventor Name Search Result

Your Search was:

Last Name = KOOTSTRA

First Name = LEWIS

Application#	Patent#	Status	Date Filed	Title	Inventor Name
<u>10091691</u>	Not Issued	30	03/05/2002	System and method for forwarding packets	KOOTSTRA, LEWIS S.
<u>10091874</u>	Not Issued	94	03/05/2002	FORWARDING ORDERED DATA PACKETS WITH LESS DELAY	KOOTSTRA, LEWIS S.
<u>10092155</u>	Not Issued	30	03/05/2002	Method and apparatus for computing, storing, and multiplexing modified bytes in a packet router	KOOTSTRA, LEWIS S.
<u>11051422</u>	Not Issued	30	02/04/2005	Conduit port for network chips	KOOTSTRA, LEWIS S.
<u>11054646</u>	Not Issued	20	02/08/2005	Support chip for handling network chips on a network device	KOOTSTRA, LEWIS S.
<u>11088394</u>	Not Issued	30	03/23/2005	Encapsulating packets for network chip conduit port	KOOTSTRA, LEWIS S.
<u>11103966</u>	Not Issued	20	04/12/2005	Priority aware queue	KOOTSTRA, LEWIS S.
<u>11114253</u>	Not Issued	20	04/26/2005	Item queue management	KOOTSTRA, LEWIS S.
<u>07292590</u>	5051888	250	12/30/1988	DATA PROCESSING SYSTEM FOR COORDINATING MEASUREMENT ACTIVITY UPON PALURURALITY OF EMULATORS	KOOTSTRA, LEWIS S.
<u>07422877</u>	Not Issued	161	10/17/1989	METHOD AND APPARATUS FOR COORDINATING MEASUREMENT ACTIVITY UPON A PLURALITY OF EMULATORS	KOOTSTRA, LEWIS S.
<u>07718728</u>	5202976	250	06/21/1991	METHOD FOR SIMULTANEOUSLY BEGINNING EXECUTION OF	KOOTSTRA, LEWIS S.

				USER CODE UPON A PLURALITY OF EMULATORS	
<u>08006571</u>	Not Issued	161	01/21/1993	ANALYZER CONTROL BUS WITH SIGNAL LINE HAVING CONTEXT DEPENDENT MEANING	KOOTSTRA, LEWIS S.
<u>10091775</u>	Not Issued	30	03/05/2002	System and method for speculatively issuing memory requests while maintaining a specified packet order	KOOTSTRA, LEWIS S.
<u>10091837</u>	Not Issued	30	03/05/2002	System for performing input processing on a data packet	KOOTSTRA, LEWIS STEPHEN
<u>10109258</u>	<u>6880056</u>	150	03/28/2002	MEMORY ARRAY AND METHOD WITH SIMULTANEOUS READ/WRITE CAPABILITY	KOOTSTRA, LEWIS STEPHEN
<u>10146344</u>	Not Issued	95	05/15/2002	COMBINING COMMANDS TO FORM A TEST COMMAND	KOOTSTRA, LEWIS STEPHEN
<u>11105088</u>	Not Issued	30	04/11/2005	Memory array and method with simultaneous read/write capability	KOOTSTRA, LEWIS STEPHEN

Inventor Search Completed: No Records to Display.

Search Another: Inventor	Last Name	First Name	Search
	<input type="text" value="KOOTSTRA"/>	<input type="text" value="LEWIS"/>	

To go back use Back button on your browser toolbar.

Back to [PALM](#) | [ASSIGNMENT](#) | [OASIS](#) | [Home page](#)

Day : Tuesday
Date: 11/22/2005


PALM INTRANET

Time: 12:22:48

Inventor Name Search Result

Your Search was:

Last Name = GOOCH

First Name = MARK

Application#	Patent#	Status	Date Filed	Title	Inventor Name
<u>10091691</u>	Not Issued	30	03/05/2002	System and method for forwarding packets	GOOCH, MARK
<u>10091837</u>	Not Issued	30	03/05/2002	System for performing input processing on a data packet	GOOCH, MARK
<u>10092159</u>	Not Issued	30	03/05/2002	Method and system for parallel hash transformation for an address input	GOOCH, MARK
<u>10098984</u>	Not Issued	30	03/14/2002	Method and system for performing flow based hash transformation to generate hash pointers for a network device	GOOCH, MARK
<u>10102990</u>	Not Issued	71	03/20/2002	Method and system for buffering multiple requests from multiple devices to a memory	GOOCH, MARK
<u>10104888</u>	Not Issued	30	03/21/2002	Method and system for performing a hash transformation to generate a hash pointer for an address input by using rotation	GOOCH, MARK
<u>10648148</u>	Not Issued	30	08/25/2003	Diversified host based route selection metric	GOOCH, MARK
<u>10723041</u>	Not Issued	20	11/26/2003	Remote mirroring using IP encapsulation	GOOCH, MARK
<u>10813730</u>	Not Issued	20	03/31/2004	Secure remote mirroring	GOOCH, MARK
<u>10813766</u>	Not Issued	30	03/31/2004	Methods and apparatus for selection of mirrored traffic	GOOCH, MARK
<u>11198056</u>	Not Issued	30	08/05/2005	Prioritization of network traffic sent to a processor by using packet importance	GOOCH, MARK
<u>11207357</u>	Not Issued	20	08/18/2005	Data corruption scrubbing for content addressable memory and ternary content addressable memory	GOOCH, MARK

Inventor Search Completed: No Records to Display.

	Last Name	First Name	
Search Another: Inventor	<input type="text" value="GOOCH"/>	<input type="text" value="MARK"/>	<input type="button" value="Search"/>

To go back use Back button on your browser toolbar.

Back to [PALM](#) | [ASSIGNMENT](#) | [OASIS](#) | [Home page](#)